# CL1208 Multi-phase power inductors



#### **Product features**

- High current multi-phase inductor
- 100 nH per phase coupled inductor
- 12 mm wide x 8.5 mm high footprint surface mount package with 12.5 mm, 18.5 mm, 24.5 mm and 36.5 mm lengths
- · Ferrite core material
- Moisture Sensitivity Level (MSL): 1

#### Applications

- For exclusive use with Maxim® Multi-phase controllers
- Voltage Regulator Modules (VRMs) and high power density VRMs
  - Server and desktop
  - Central processing unit (CPU)
  - Graphics processing unit (GPU)
  - Application specific integrated circuit (ASIC)
- Data networking and storage systems
- High current Point-of-Load (POL) modules
- Vcore regulators

### **Environmental data**

- Storage temperature range (Component): -40 °C to +125 °C
- Operating temperature range: -40 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020 (latest revision) compliant
- Halogen free, lead free, RoHS compliant



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### **Product specifications**

Part Number⁵	Inductor Phases	OCL <sup>1</sup> (nH) typical	OCL <sup>1</sup> (nH) minimum	FLL <sup>2</sup> (nH) minimum	I <sub>sat</sub> 1² (A)	DCR (mΩ) maximum @ 20 °C	SCL <sup>3</sup> (nH) ±20%	I <sub>sat</sub> 2 <sup>4</sup> (A)
CL1208- 2- 100TR-R	2	400	360	320	15	0.45	100	56
CL1208- 3- 100TR-R	3	400	360	320	15	0.45	100	56
CL1208- 4- 100TR-R	4	400	360	320	15	0.45	100	56
CL1208- 6- 100TR-R	6	400	360	320	15	0.45	100	56

1. Open Circuit Inductance (OCL) Test Parameters: 1.0 MHz, 0.1 V<sub>ms</sub>, 0.0 Adc, +105 °C

 Full Load Inductance (FLL) Test Parameters: 1.0 MHz, 0.1 V<sub>ms</sub> / <sub>saf</sub> 1, +105 °C
Short Circuit Inductance (SCL) Test Parameters: 1.0 MHz, 0.1 V<sub>ms</sub> / 0.0 Adc, +105 °C CL1208-2-100TR-R short (1 & 4), measure (2 & 3), and divide by 2

CL1208-3-100TR-R short (1 & 4), (3 & 6), measure (2 & 5), and divide by 3

CL1208-4-100TR-R short (1 & 4), (3 & 6), (5 & 8) measure (2 & 7), and divide by 4

CL1208-6-100TR-R short (1 & 4), (3 & 6), (5 & 8), (7 & 10), (9 & 12) measure (2 & 11), and divide by 6

4. I\_\_\_2: Peak current where SCL drops approximately 20% @ +105 °C

8. Part Number Definition: CL1208-x-100TR-R

CL1208 = Product code and size

x= Number of phases

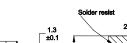
100= Inductance value per phase in nH

TR= Tape and reel packaging

-R suffix = RoHS compliant

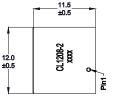
Note: This device is licensed for use only when incorporated within a voltage regulator employing power regulating devices manufactured by Maxim Integrated Devices, Inc. No license is granted expressly or by implication to use this device with power regulating devices manufactured by any company other than Maxim.

### **Dimensions (mm)**

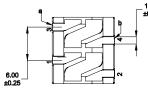


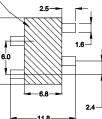
12.0



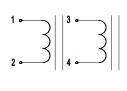


8.0 ±0.5



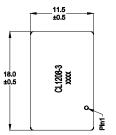


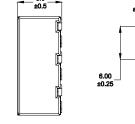
Recommended Pad Layout

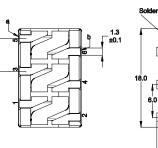


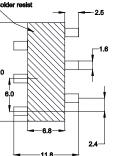
Recommended Pad Layout

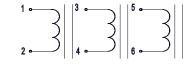
Schematic







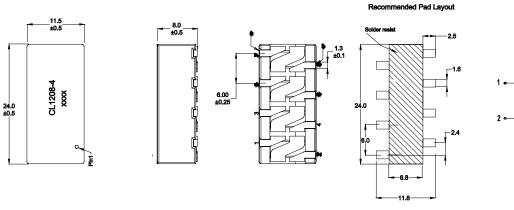


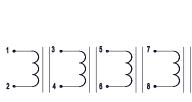


Part marking: CL1208-x-100TR-R (x = number of phases), xxxx = lot code Tolerances are ±0.25 millimeters unless stated otherwise All soldering surfaces to be coplanar within 0.13 millimeters PCB tolerances are ±0.1 millimeters unless stated otherwise DCR measured from point "a" to point "b' Do not route traces or vias underneath the inductor

# CL1208 Multi-phase power inductors

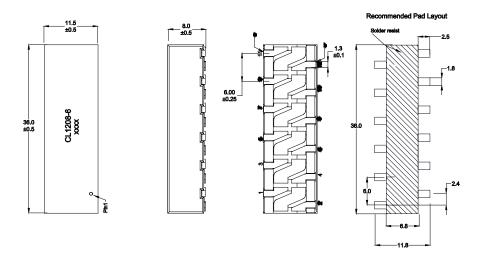
# **Dimensions (mm)**





Schematic

Schematic





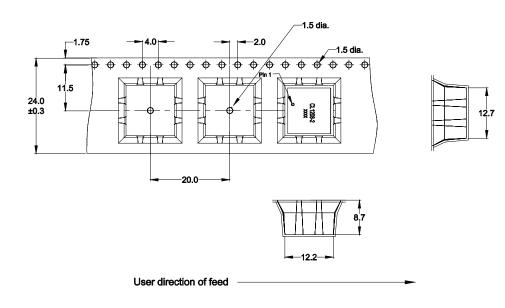
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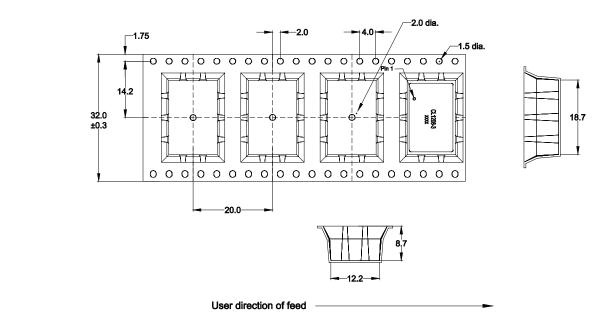
# Packaging information (mm)

Supplied in tape and reel packaging on a 13" diameter reel Drawing not to scale

# CL1208-2-100TR-R

300 parts per reel





# CL1208-3-100TR-R

300 parts per reel

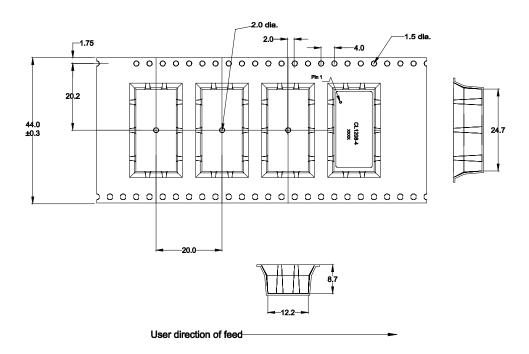
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# Packaging information (mm)

Supplied in tape and reel packaging on a 13" diameter reel Drawing not to scale

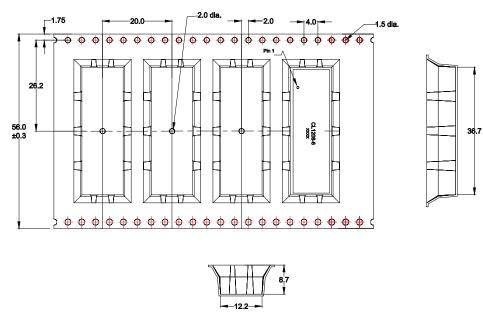
# CL1208-4-100TR-R

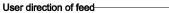
200 parts per reel



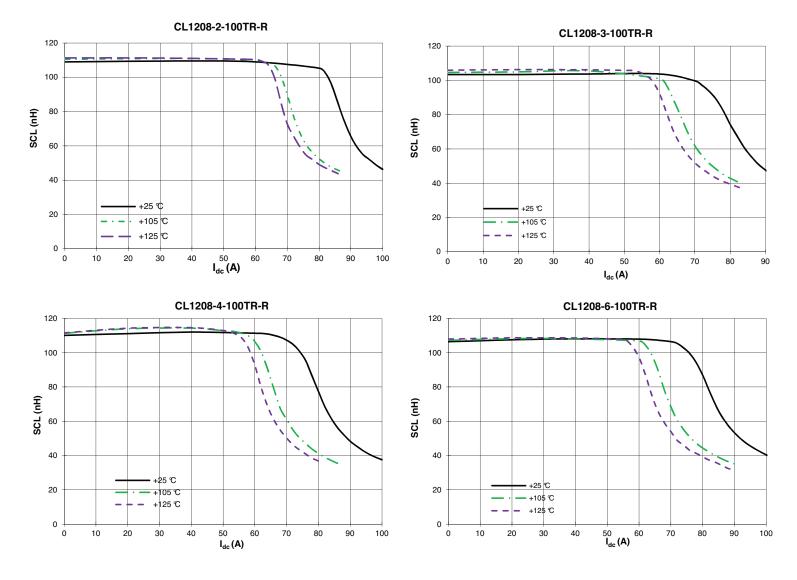
CL1208-6-100TR-R

200 parts per reel

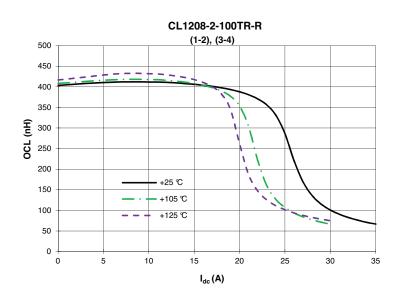


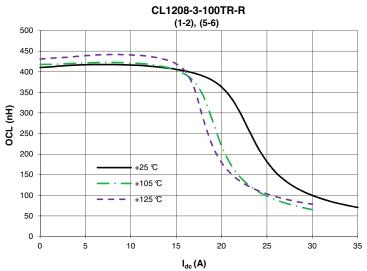


## Inductance characteristics – SCL vs. current

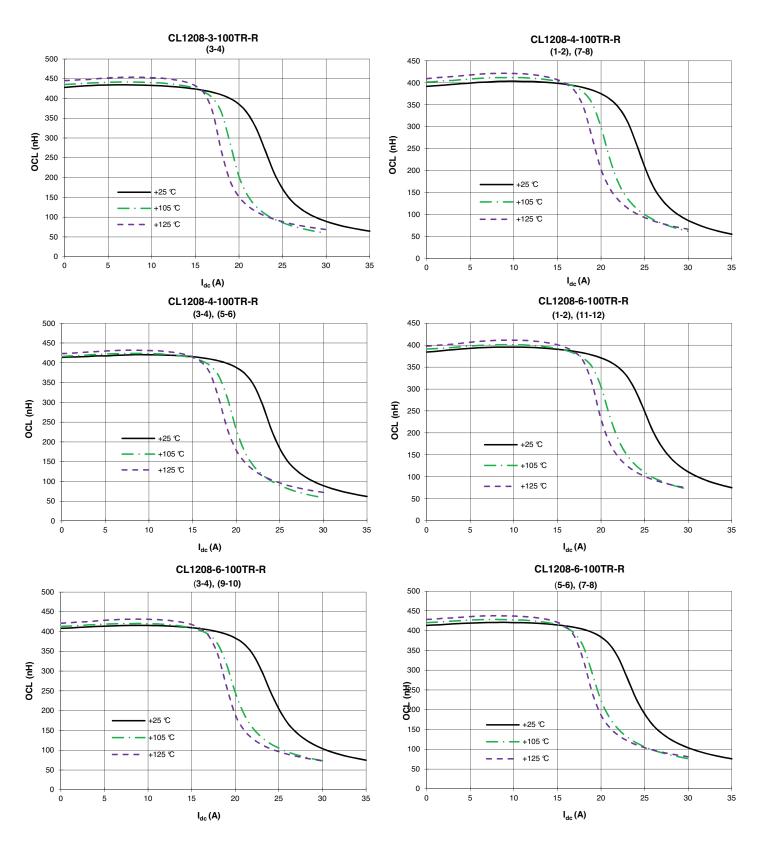




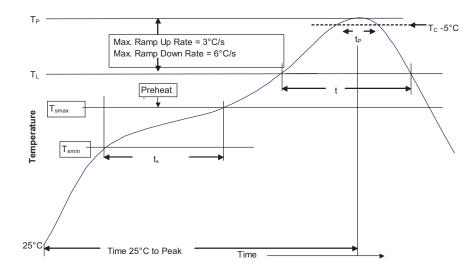




## Inductance characteristics – OCL vs. current



## Solder reflow profile



#### Table 1 - Standard SnPb Solder (T<sub>c</sub>)

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm³ ≥350
<2.5mm)	235 °C	220 °C
≥2.5mm	220 °C	220 °C

### Table 2 - Lead (Pb) Free Solder (T<sub>c</sub>)

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350 - 2000	Volume mm <sup>3</sup> >2000
<1.6mm	260 °C	260 °C	260 °C
1.6 – 2.5mm	260 °C	250 °C	245 °C
>2.5mm	250 °C	245 °C	245 °C

#### **Reference JDEC J-STD-020**

Profile Feature	Standard SnPb Solder	Lead (Pb) Free Solder	
Preheat and Soak • Temperature min. (T <sub>smin</sub> )	100 °C	150 °C	
• Temperature max. (T <sub>smax</sub> )	150 °C	200 °C	
• Time (T <sub>smin</sub> to T <sub>smax</sub> ) (t <sub>s</sub> )	60-120 Seconds	60-120 Seconds	
Average ramp up rate T <sub>smax</sub> to T <sub>p</sub>	3 °C/ Second Max.	3 °C/ Second Max.	
Liquidous temperature (TL) Time at liquidous (tL)	183°C 60-150 Seconds	217°C 60-150 Seconds	
Peak package body temperature (T <sub>P</sub> )*	Table 1	Table 2	
Time $(t_p)^{**}$ within 5 °C of the specified classification temperature $(T_c)$	20 Seconds**	30 Seconds**	
Average ramp-down rate (T <sub>p</sub> to T <sub>smax</sub> )	6 °C/ Second Max.	6 °C/ Second Max.	
Time 25 °C to Peak Temperature	6 Minutes Max.	8 Minutes Max.	

\* Tolerance for peak profile temperature (T<sub>p</sub>) is defined as a supplier minimum and a user maximum.

\*\* Tolerance for time at peak profile temperature (t<sub>n</sub>) is defined as a supplier minimum and a user maximum.

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Eaton Electronics Division 1000 Eaton Boulevard Cleveland, OH 44122 United States www.eaton.com/electronics

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